

Designing Gain and Offset in Thirty Seconds

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ABSTRACT

This document discusses how to design an operational amplifier (op amp) with both gain and dc offset. The design requires no theory and very little math, just working designs.

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1 Introduction

This document is intended for designers that have an input source with a voltage range and dc offset that are incompatible with the load, which must be referenced to a different dc offset and requires a different voltage range.

To design such a circuit, some things must be known in advance:

- The voltage level of a stable reference, Vref = _____
- The full-scale output voltage, V_{out}fs = _____
- The zero-scale output voltage, V_{out}zs = _____
- The full-scale input voltage, V_{in}fs = _____
- The zero-scale input voltage, V_{in}zs = _____

There is a companion Engineering Design Utility for this application note, available in the Engineer Design Utilities in the Amplifiers and Comparators section of the Analog and Mixed Signal link from the Texas Instruments web page. Appendix C describes this utility.

Designing the gain and offset stage

2 Determining the Type of Function

Determining the type of function depends on the sign of two numbers: m (the gain of the stage) and b (the offset of the stage) – that are calculated now.

- Calculate $m = \frac{V_{out}fs V_{out}zs}{V_{in}fs V_{in}zs} =$ _____
- Calculate $b = V_{out}zs m \times V_{in}zs =$
- Go to the section determined by the sign of the numbers m and b
 - Positive m and positive b go to Section 3
 - Positive m and negative b go to Section 4
 - Negative m and positive b go to Section 5
 - Negative m and negative b go to Section 6

3 Positive m and Positive b

To determine positive m and positive b;

- Choose R1 = _____
- Calculate R2 = $\frac{Vref \times R1 \times m}{b}$ = _____
- Select Rf (may be suggested by data sheet) = _____

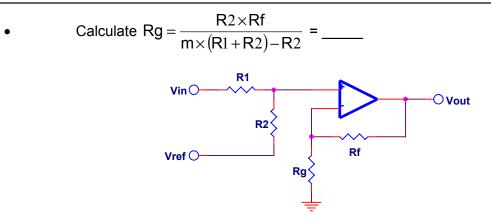


Figure 1. Schematic Diagram for Positive m and Positive b

Refer to Section 7

4 Positive m and Negative b

Determining positive m and negative b may take a little longer.

NOTE: The following is an approximation, which assumes R1 >> Rg2

- Choose Rf (may be suggested on data sheet). Rf = _____
- Calculate $Rg = \frac{Rf}{m-1} =$ ____
- Choose Rg2 $\approx \frac{\text{Rg}}{10}$ = _____
- Calculate Rg1 = Rg Rg2 = _____
- Calculate Vref' = $\frac{|b| \times Rg1}{Rg1 + Rf}$ = _____
- Calculate $R1 = \frac{Rg2(Vref Vref')}{Vref'} =$ _____

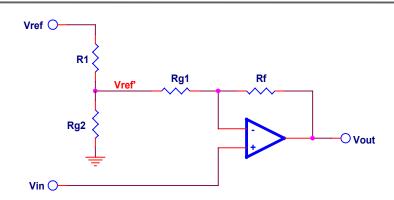


Figure 2. Schematic Diagram for Positive m and Negative b

Tuning

This is the point at which the approximation accumulates errors. It may be necessary to *tweak* the value of R1 a bit to compensate for errors that the approximation yields. If more accuracy is required, it is necessary to eliminate the interaction of gain resistors.

There are two ways to proceed if a more accurate answer is required.

Use the companion Engineering Design Utility for this application note, available in the Engineer Design Utilities in the Amplifiers and Comparators section of the Analog an Mixed Signal link from the Texas Instruments web page. Appendix C describes this utility.

The second way to achieve more accuracy is by adding a second op amp:

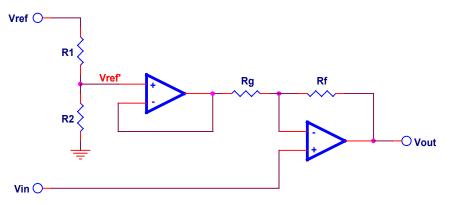


Figure 3. Schematic for Enhanced Accuracy of Positive m and Negative b

To use the enhanced accuracy schematic:

- Select Rf (may be suggested on data sheet) = _____
- Calculate $Rg = \frac{Rf}{m-1} =$ _____

- Calculate Vref'= $\frac{|\mathbf{b}|}{\mathbf{m}}$ =_____
- Select R1 = _____

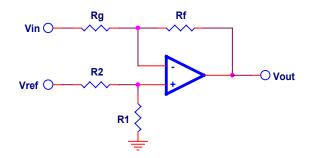
• Calculate
$$R2 = \frac{Vref' \times R1}{Vref - Vref'} =$$

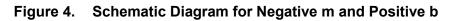
When you are finished with the enhanced accuracy schematic, go to Section 7

5 Negative m and Positive b

Selecting negative value for m and positive value for b;

- Choose Rf (may be suggested on data sheet). Rf = _____
- Calculate $Rg = \frac{Rf}{|m|} =$ _____
- Choose R2 (same order of magnitude as Rf). R2 = _____
- Calculate $R1 = \frac{b \times R2 \times Rg}{Vref \times (Rf + Rg) b \times Rg} =$ _____





Refer to Section 7

6 Negative m and Negative b

Selecting negative value for m and positive value for b;

- Choose Rf (may be suggested on data sheet). Rf = _____
- Calculate Rg1 = $\frac{\text{Rf}}{|\mathbf{m}|}$ = _____

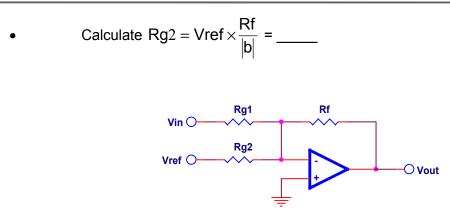


Figure 5. Schematic Diagram for Negative m and Negative b

7 Filtering

Simultaneous gain, offset, and filtering in one op amp are possible. Find the case below.

7.1 Positive m and Positive b With Filtering

Given a rolloff frequency f_o , the value of Co can be determined by:

$$Co = \frac{1}{2 \times \pi \times R1 \times f_o} =$$

Figure 6. Positive m and Positive b With Filtering

NOTE: The reference voltage will also be rolled off at a frequency determined by:

 $fo = \frac{1}{2 \times \pi \times R2 \times C_o} = \underline{\qquad}$

7.2 Positive m and Negative b With Filtering

Given a rolloff frequency f_o:

Select a value for Ro = _____



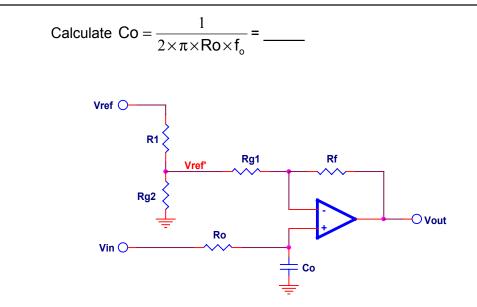
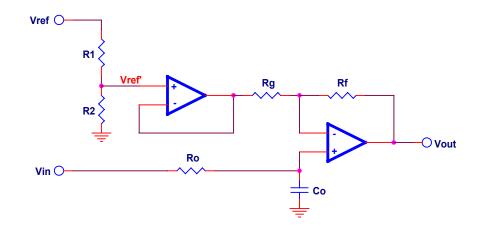


Figure 7. Positive m and Negative b With Filtering

7.3 Enhanced Accuracy Positive m and Negative b With Filtering

Given a rolloff frequency f_o:

- Select a value for Ro = _____
- Calculate $Co = \frac{1}{2 \times \pi \times Ro \times f_o} =$ _____





7.4 Negative m and Positive b With Filtering

Given a rolloff frequency f_o , the value of Cf can be determined by:

$$Cf = \frac{1}{2 \times \pi \times Rf \times f_o} = ---$$



7.5 Negative m and Negative b With Filtering

Given a rolloff frequency $f_{\text{o}},$ the value of Cf can be determined by:

 $Cf = \frac{1}{2 \times \pi \times Rf \times f_o} = \underline{\qquad}$

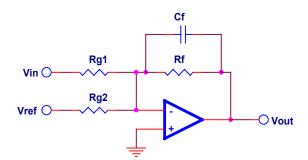


Figure 10. Negative m and Negative b With Filtering

References

1. <u>Chapter 4 - Single-Supply Op Amp Design Techniques</u>, Texas Instruments SLOA076 - 09/14/2001

Appendix A – Standard Resistor and Capacitor Values

E-12 Resistor / Capacitor Values

1.0, 1.2, 1.5, 1.8, 2.2, 2.7, 3.3, 3.9, 4.7, 5.6, 6.8, and 8.2; multiplied by the decade value.

E-24 Resistor / Capacitor Values

1.0, 1.1, 1.2, 1.3, 1.5, 1.6, 1.8, 2.0, 2.2, 2.4, 2.7, 3.0, 3.3, 3.6, 3.9, 4.3, 4.7, 5.1, 5.6, 6.2, 6.8, 7.5, 8.2, and 9.1; multiplied by the decade value.

E-96 Resistor Values

1.00, 1.02, 1.05, 1.07, 1.10, 1.13, 1.15, 1.18, 1.21, 1.24, 1.27, 1.30, 1.33, 1.37, 1.40, 1.43, 1.47, 1.50, 1.54, 1.58, 1.62, 1.65, 1.69, 1.74, 1.78, 1.82, 1.87, 1.91, 1.96, 2.00, 2.05, 2.10, 2.15, 2.21, 2.26, 2.32, 2.37, 2.43, 2.49, 2.55, 2.61, 2.67, 2.74, 2.80, 2.87, 2.94, 3.01, 3.09, 3.16, 3,24, 3.32, 3.40, 3,48, 3.57, 3.65, 3.74, 3.83, 3.92, 4.02, 4.12, 4.22, 4,32, 4.42, 4,53, 4.64, 4.75, 4.87, 4.99, 5.11, 5.23, 5.36, 5.49, 5.62, 5.76, 5.90, 6.04, 6.19, 6.34, 6.49, 6.65, 6.81, 6.98, 7.15, 7.32, 7.50, 7.68, 7.87, 8.06, 8.25, 8.45, 8.66, 8.87, 9.09, 9.31, 9.53, 9.76; multiplied by the decade value.

Appendix B – Simultaneous Equations (for the More Technically Minded)

A linear op amp transfer function is described by the equation of a straight line (Equation 1).

$$y = \pm mx \pm b \tag{1}$$

where m is the slope of the line, and b is the intercept of the line

The equation of a straight line has four possible solutions depending upon the sign of m and b; thus simultaneous equations yield solutions in four forms. Four circuits must be developed; one for each form of the equation of a straight line. The four equations are given in Equations 2 through 5.

y = +mx + b	(2)

$$y = +mx - b \tag{3}$$

$$\mathbf{y} = -\mathbf{m}\mathbf{x} + \mathbf{b} \tag{4}$$

$$y = -mx - b \tag{5}$$

The sign of m and b determines the type of circuit required to implement the solution.

The designer can easily determine the value and sign of m by the following equation:

$$m = \frac{V_{out}fs - V_{out}zs}{V_{in}fs - V_{in}zs}$$
(6)

Where:

Voutfs is the full-scale output voltage

Voutzs is the zero-scale output voltage

Vinfs is the full-scale input voltage

Vinzs is the zero-scale input voltage

Consider the example of a sensor with an output signal ranging from 0.1 to 0.2 Vdc, being required to interface to an analog to digital converter with an input range of 1 to 4 Vdc.

$V_{in}zs = 0.1 Vdc$	(7)
V_{in} fs = 0.2 Vdc	(8)
V _{out} zs = 1 Vdc	(9)
V _{out} fs = 4 Vdc	(10)
Therefore:	

$m = \frac{4-1}{3} = \frac{3}{30} = 30$	(11)
0.2 - 0.1 0.1	()

This limits the transfer function for the op amp to either equation 2 or 3 - the ones with positive values of m.

The value and sign of b can be determined by plugging the value of m into equation 2. If the guess about the sign of b is wrong, the math is self-correcting and the correct sign will be revealed.

1 = 30 * 0.1 + b	(12)
1 – 3 = b	(13)
b = -2	(14)
The correct form of the equation of the example straight line is therefore:	

$$\mathbf{y} = +\mathbf{m}\mathbf{x} - \mathbf{b} \tag{15}$$



Appendix C – Engineering Design Utility for the Positive m and Negative b Case

Figure 11 shows a screen capture of the case selection page of the utility, which is implemented as a Microsoft Excel_® Spreadsheet.

Component Calculator for Op Amp Gain and Offset Stage Case Selection Sheet					
by Bruce Carter version 3, 1-24-2002					
Enter Values in Yellow:	General equation for a line:	y = +/- mx +/- b			
VinZS 0.2 Volts VinFS 0.5 Volts VoutZS 1.5 Volts VoutFS 4.5 Volts Vref 5 Volts	m = b =	10 -0.5			
Go to Case 2					

Figure 11. Component Calculator for the Positive m and Negative b Case

The utility is pre-loaded with examples from Section 4.3 of Op Amps for Everyone. Chapter 4 is also available as a stand-alone application note (reference 1). The way the designer uses this utility:

- Enter the 5 values called for in the yellow boxes of the case selection page of the utility; V_{in}zs, V_{in}fs, V_{out}zs, V_{out}fs, and V_{ref}.
 - Go to the case indicated by the prompt on the case selection page.

When the designer goes to the appropriate case, they will be prompted for one or more "seed" values of resistor. Figure 12 shows one of the four cases:

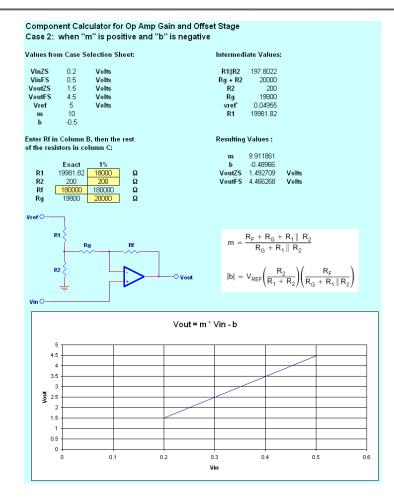


Figure 12. Case 2 Calculation Screen

Data entry is done in two columns, in this case the field for Rf is shown in yellow in the left column, and is the only column required to generate the other three resistor values. Data entry is made from a drop-down list, which has the 5% resistor sequence followed by the 1% resistor sequence. When the seed resistor value is selected from the drop-down list in the left column, the designer then selects the other resistors in the right hand column – also from drop-down lists, finding the standard value closest to the calculated value.

In case 2, shown here, the offset is slightly off with the calculated values (the algorithm is an approximation). The offset can be adjusted with very little interaction on the gain "m" by varying R1. When it is reduced to $18 \text{ k}\Omega$, the offset is very close to ideal.

The resulting "real world" values calculated for m, b, $V_{out}zs$, and $V_{out}fs$ are shown to the right of the resistor selection boxes, and a graph is shown underneath of the transfer function for the line.

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