PIC18F4620

A special variant of MIOS is available for the PIC18F4620. The usage of this processor is (currently) only required for the next major step of MIDIbox SEQ (V3), most other projects are running fine on a PIC18F452 *(MIDIbox SID V2 will run on a PIC18F4685)*

Please note, that Rev A3 and A4 of the chip are affected by the EUSART Bug. It has been fixed in revision B4 (meanwhile we reached B5)

The PIC18F4620 is almost software compatible to existing MIOS applications. There is a special MIOS version which needs to be uploaded, the major differences are different memory limit checks (e.g. code upload to addresses >= 0x8000 are possible).

Spec

- Internal Flash: 64k
- SRAM: 3968 bytes
- Internal EEPROM: 1024 bytes

FAQ

Do I need a special Core Module for the PIC18F4620?

No, the PIC18F4620 is pin-, hard- and softwarecompatible to the PIC18F452

Can I run an old PIC18F452 Application on a PIC18F4620?

Yes, all applications are binary compatible, which means, that you don't even need to build a new .hex file, just upload the precompiled code. The Bootloader and MIOS have been changed slightly to allow programs to use the upper flash area and extended EEPROM range. Special versions of the MIOS update package are found in the pic18f4620/ directory

Can I use my old JDM burner to burn the bootloader onto a PIC18F4620?

Yes. Try JDM with Winpic800

Can I use my old JDM burner to burn the IIC MIDI firmware onto a PIC16F88?

Yes. Try JDM with Winpic800

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